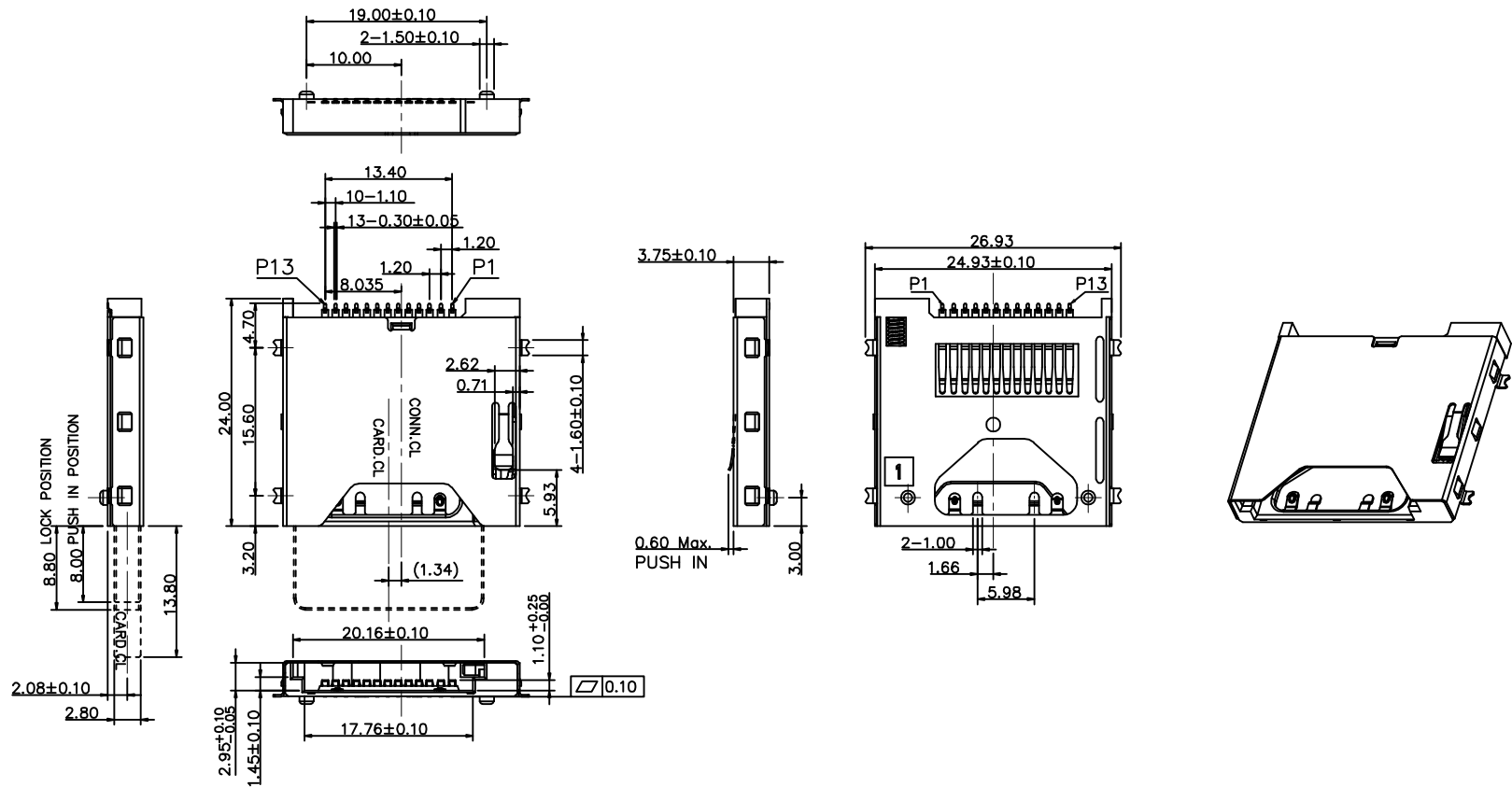


更改 REV	变更内容 MODIFICATION
A	ECXXXX



SPECIFICATION

1.Material:

Housing: LCP Rated UL94V-0,Color:Black.
 Contacts: Phosphor bronze.
 Matte Tin 80u"MIN. at Solder Tail,
 Plating 15U" on Contact Area.Over Nickle 50u"MIN.
 Shell: SUS304 1/2H.
 Goldflash at Solder Tail, Over Nickel 30u"MIN.Plated.
 LOCK PIN: SUS304 3/4H. Not Plating.
 SPRING: SWP-B. Not Plating.
 CAM ROD: SUS304H. Not Plating.

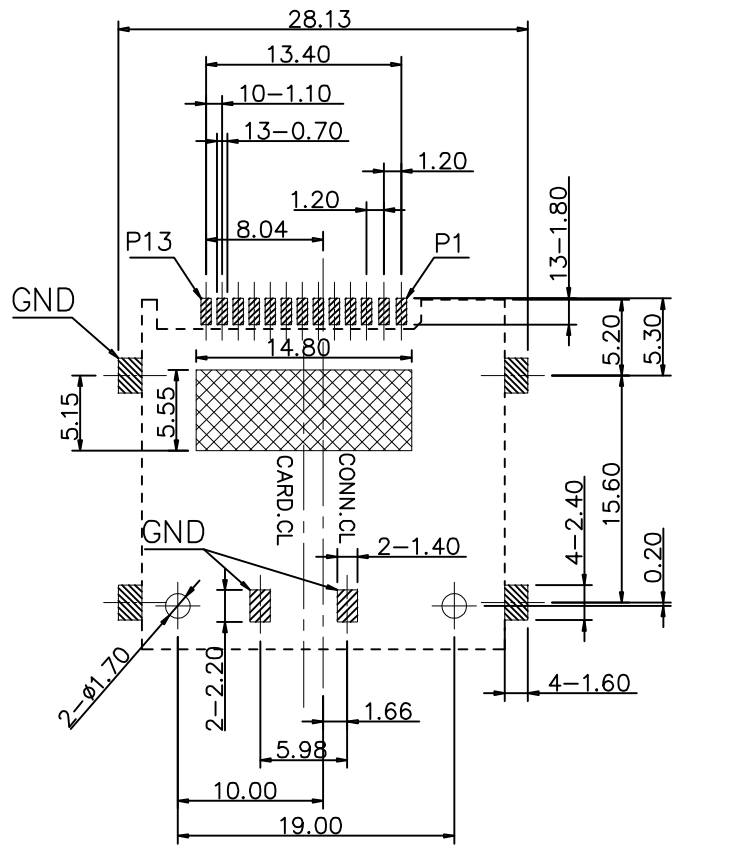
2.Electrical Characteristics:

Rated voltage : 500Vrms AC for 1 min.
 Current rating : 1.0 A.Min.per pin.
 Operating Temperature: -25°C~+85°C.
 Insulation resistance: 1000M ohms Min. at 500VDC for 1ms.
 Dielectric withstanding voltage:500 VAC/1minute.
 Contact resistance: Initial:55m ohms max. Final: Δ 40m ohms max.
 Insertion Force: 25N Max.
 Removal Force: 0.5N Min. 20N Max.
 Inverse Insertion: 50N Min.
 Mating cycle: 10000 cycles, Spring is not breaking.

GENERAL TOLERANCE		KINGCONN 皇海科技股份有限公司	
X± 0.50	X*± 5 °	名 稱(TITLE) CF EXPRESS TYPE A PUSH 15u"	
.X± 0.30	.X*± 2 °		
.XX± 0.20	.XX*± 1 °		
單 位(UNIT)	料 號(PART NO.)	圖 號(DWG NO.)	
mm	7CFEA-C0-0001	7CFEA-C0-0001	
審 核(APPROVAL)	核 對(CHECKED)	製 圖(DRAWN)	比 例 SCALE 1:1 圖 數 SHEET 1/1 製 圖 日期 DATE 更 改 REV A



更改 REV	修改内容 MODIFICATION
A	ECXXXX



RECOMMENDED P.C.B LAYOUT
TOLERANCE IS: ±0.05

- PAD AREA
- KEEP OUT AREA
- NO COPPER AREA
(NO Trace & Via GND)
- GND PATTERN ONLY

PIN DESIGN		
13	GND	Ground
12	PETp0	PCIe Gen3 Tx singal
11	PETn0	PCIe Gen3 Tx singal
10	GND	Ground
9	PERp0	PCIe Gen3 Rx singal
8	PERn0	PCIe Gen3 Rx singal
7	GND	Ground
6	REFCLK+	Reference Clock input
5	REFCLK-	Reference Clock input
4	INS#	Media detection and power control
3	CLKREQ#	Indication of REFCLK to Host
2	+3.3V	3.3V Power Supply
1	PERST#	PCIe Express function reset



GENERAL TOLERANCE		KINGCONN 皇海科技股份有限公司
X± 0.50	X°± 5°	
.X± 0.30	.X°± 2°	
.XX± 0.20	.XX°± 1°	名 稱(TITLE) CF EXPRESS TYPE A PUSH 15U"
單位(UNIT) mm	料 號(PART NO.) 7CFEA-C0-0001	圖 號(DWG NO.) 7CFEA-C0-0001
審 核(APPROVAL)	核 對(CHECKED)	製 圖(DRAWN)
		比 例 SCALE 1:1
		張 數 SHEET 2/2
		圖 號 REV A